

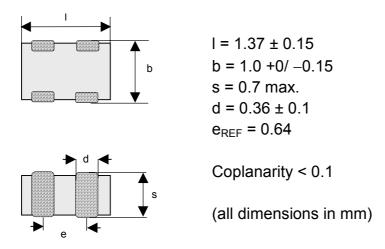
SMD multilayer varistor array with Ni-barrier termination

CA04P2S17TLCG B72762A2170S160

Designation system

- CA = <u>C</u>hip <u>a</u>rray
- = Dimensions of the device $\underline{04} \times 05$ (length x width in 1/100 inch)
- P = Design (parallel internal structure)
- 2 = Number of elements
- S = <u>S</u>pecial tolerance of the varistor voltage
- 17 = Maximum operating voltage
- $T = \underline{T}$ hree layer terminations
- LC = <u>L</u>ow <u>c</u>apacitance
- G = Taped version (cardboard tape, 7" reel, 4000 pieces/reel)

Figure



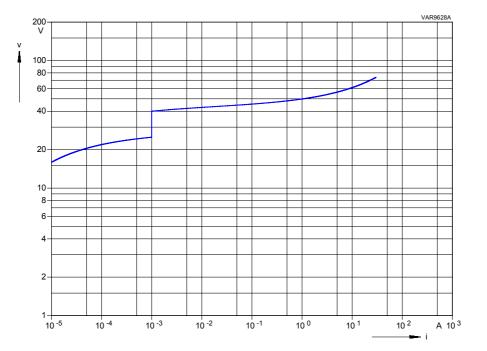
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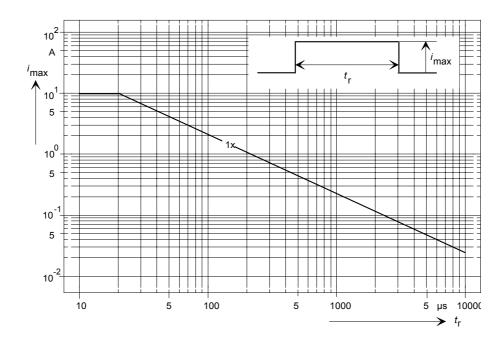
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V-I-characteristic



Derating field





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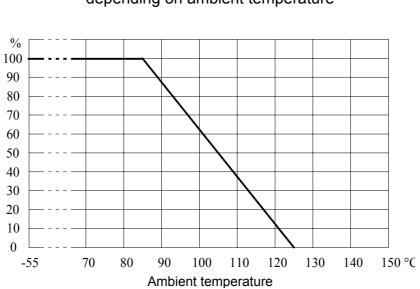
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Electrical data

Maximum operating voltage	
RMS voltage	V _{RMS} = 17 V
DC voltage	$V_{DC} = 22 V$
Varistor voltage (@ 1 mA)	V _v = 25 up to 40 V
Maximum clamping voltage (@ 1 A)	$V_c = 50 V$
Maximum average power dissipation	P _{max} = 3 mW
Maximum surge current (8/20 µs)	I _{max} = 1 x 10 A
Maximum energy absorption (2 ms)	E _{max} = 1 x 0.01 J
Capacitance (@ 1 MHz, 0.5 V)	< 75 pF
Response time	< 0.5 ns
Operating temperature	–30 … +85 °C
Storage temperature (mounted parts)	–40 … +125 °C
Termination material	Ag/Ni/Sn
(thickness not specified, adjusted to fulfill wet	tability specification according to
DIN IEC 68-2-58)	

Part weight

0.004 g



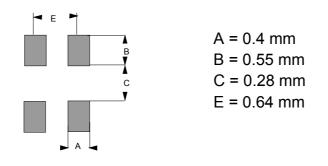
Max. current, energy and average power dissipation depending on ambient temperature

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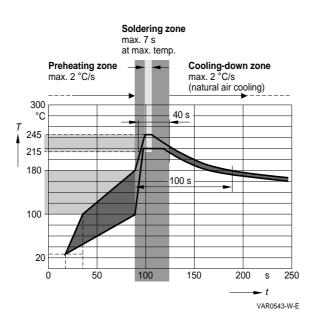


Metal Oxide VaristorsCA04P2S17TLCGSMD multilayer varistor array with Ni-barrier terminationB72762A2170S160

Recommended geometry of solder pad



Recommended soldering temperature profile



This component is suited for reflow soldering. Maximum reflow cycles: 2 x

As far as possible, the components shall be employed within 6 months. They should be left in their original packings to avoid soldering problems due to oxidized terminals. Storage temperature: -25 to 45°C

Relative humidity: < 75% annual average, < 95% on maximum 30 days in a year.

The usage of mild non-activated fluxes for soldering is recommended, as well as proper cleaning of the PCB.

The components are suited for Pb-free soldering.

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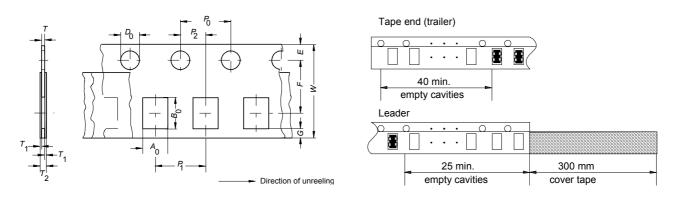


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Taping according to IEC 60286-3

Tape material: cardboard



Dimensions and tolerances:

Definition	Symbol	Dimension	Tolerance
		[mm]	[mm]
Compartment width	A ₀	1.05	± 0.05
Compartment length	B ₀	1.57	± 0.05
Sprocket hole diameter	D ₀	1.5	+0.1/ -0
Sprocket hole pitch	P ₀	4.0	± 0.1 ¹⁾
Distance center hole to center compartment	P ₂	2.0	± 0.05
Pitch of the component compartments	P ₁	4.0	± 0.1
Tape width	W	8.0	± 0.3
Distance edge to center of hole	Е	1.75	± 0.1
Distance center hole to center compartment	F	3.5	± 0.05
Distance compartment to edge	G	0.75	min.
Thickness of cardboard tape	Т	0.75	± 0.2
Overall thickness	T ₂	0.9	max.

 $^{1)} \le \pm 0.2$ mm over any 10 pitches

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SMD multilayer varistor array with Ni-barrier termination

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Packing

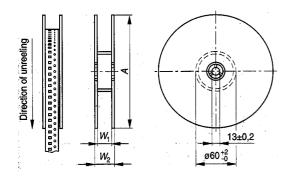
Each reel in airtight plastic bag with desiccant bag.

Dimensions approx. 220 x 200 mm. Weight approx. 170 g.

Package: 8 mm tape

Reel material: plastic

Reel dimensions:



Definition	Symbol	Dimension	Tolerance
		[mm]	[mm]
Reel diameter	А	180	+0/ -3
Reel width (inside)	W ₁	8.4	+1.5 /0
Reel width (outside)	W ₂	14.4	max.

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